Call for Papers

Announcing an Issue of the IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS on Foundry-Enabled Photonic Integrated Circuits

Submission Deadline: December 1, 2018
Hard Copy Publication: Sept/Oct 2019

The IEEE Journal of Selected Topics in Quantum Electronics (JSTQE) invites manuscript submissions in the area of Foundry-Enabled Photonic Integrated Circuits. The purpose of this issue of JSTQE is to highlight the recent progress and trends in foundries and associated technologies to develop volume manufacturing of photonic integrated circuits. As integrated photonics technology matures and potential market demands increase, foundries become critical for volume production. In recent years the developments in the offerings of foundry services and multi-project wafer (MPW) runs, in both silicon and InP based material systems, enable a path toward volume production of photonic integrated circuits. Fabrication in the foundry environment will also lead to more highly integrated circuits, performance improvements and lower cost.

Specific areas of interest include (but not limited to):

- Topic 1 Electronic Photonic Integration
- Topic 2 Integration of photonics in standard CMOS technology nodes
- Topic 3 Interposer platforms and die attach
- Topic 4 Photonic circuit design for volume manufacture
- Topic 5 Test for volume manufacture
- Topic 6 Wafer level package
- Topic 7 Cost models
- Topic 8 Foundry models for custom, low-volume manufacturing

The Primary Guest Editor for this issue is Madeleine Glick, Columbia University, USA. The Guest Editors are: Paul Juodawlkis, MIT-Lincoln Laboratories, USA; Dim-Lee Kwong, Institute of Microelectronics (IME): Agency for Science, Technology and Research, Singapore; Marco Romagnoli, CNIT (National Interuniversity Consortium for Telecommunications), Italy; Kevin A. Williams, Eindhoven Technische Universiteit, Netherlands; Zhiping (James) Zhou, Peking University, China.

The deadline for submission of manuscripts is December 1, 2018. Hardcopy publication of the issue is scheduled for September/October 2019.

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For inquiries, please contact:
IEEE Photonics Society JSTQE Editorial Office - Chin Tan Lutz (Phone: 732-465-5813, Email: c.tanlutz@ieee.org)

The following documents located at http://mc.manuscriptcentral.com/jstqe-pho are required during the mandatory online submission.

1) PDF manuscript (double column format, up to 12 pages for an invited paper, up to 8 pages for a contributed paper). Manuscripts over the standard page limit will have an overlength charge of $220.00 per page imposed. Biographies of all authors are mandatory, photographs are optional. See the Tools for Authors link: www.ieee.org/web/publications/authors/transjnl/index.html.

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